

10/04/2002

10/04/2002

01/14/02

ABANDONED

P

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

JC4/956

APPL NUM 10047858	FILING DATE 01/14/2002	CLASS 368/43	SUBCLASS 71	GAU 2818	EXAMINER D. G. C.
**APPLICANTS: Batinovich Victor; 439					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A CIP OF 09/915,708 07/25/2001 WHICH IS A CIP OF 09/609,626 07/03/2000					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO P1208	
Verified and Acknowledged Examiners's initials					
TITLE : Method and apparatus for vertically stacking and interconnecting ball grid array (BGA) electronic circuit devices					

BEST AVAILABLE COPY

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.				

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

Attached in socket on right inside flap